1. Consider this instruction: MOVE R1, M[$15] for a machine that has a 8-bit Data Bus, 8-bit Address Bus and 8-bit instructions. Describe all the steps of executing this single instruction in a machine with a Von Neumann architecture. Explain what parts are involved in executing this instruction and how they are involved. (All parts and their connections.)

2. Consider this instruction: ADD R1, M[R2]. Describe all the steps of executing this single instruction in a machine with a Von Neumann architecture. The machine has a 8-bit Data Bus, 8-bit Address Bus and 8-bit instructions.